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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10052779	01/23/2002	327		2816	

****APPLICANTS:** Kawasumi Atsushi;

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A DIV OF 09/449,382 11/24/1999 TDC

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**** FOREIGN APPLICATIONS VERIFIED:**
JAPAN P10-338008 11/21/1998 TDC

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY / DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		005405.00001
Verified and Acknowledged Examiners's initials TDC		
TITLE : Current mirror circuit and current source circuit		

U.S. DEPT. OF COMMERCE / PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheet to Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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